

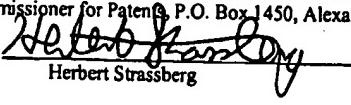


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CERTIFICATE OF MAILING BY "FIRST CLASS MAIL"

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on December 06, 2005.


Herbert Strassberg

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig et al.

Serial No.: ~~10/286,253~~ 10/046,864

Patent No.: 6,907,593 B1

Filing Date: 01/13/2002

For: METHOD AND APPARATUS FOR
PRE-COMPUTING ATTRIBUTES OF
ROUTES

Certificate

DEC 15 2005

of Correction

Examiner: Sun J. Lin

Group Art Unit: 2825

TRANSMITTAL LETTER FOR REQUEST FOR CERTIFICATE OF CORRECTION

Attention Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R., § 1.322, the Applicants submit for consideration a Request of Certificate of Correction on Form PTO/SB/44, attached hereto. There is no fee associated with this Request, as the corrected errors are not the fault of the Applicants, but are incurred through the fault of the Patent Office, which mistakes are clearly disclosed in the records of the Patent Office. However, in the unlikely event that the Patent Office determines that a fee is required,

JS

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Serial No. 10/286,584
Docket No. SPLX.P0052

DEC 16 2005

Applicants authorize the Commissioner to charge the cost of such fees to Deposit Account No.
50-1128, referencing Attorney Docket No. SPLX.P0052.

Dated: December 06, 2005

Respectfully submitted,

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTIONPage 1 of 1**PATENT NO.:** 6,907,593 B1**DATED :** Jun. 14, 2005**INVENTOR(S) :** Steven Teig et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page.

Item [57], **ABSTRACT**, reads "Some embodiments provide a method of pre-computing attributes of routes for nets in a region of a design layout. The pre-computed attributes are used by an electronic design automation application that partitions a design-layout region into a plurality of sub-region." and should read

--A method for pre-computing net route attributes for a region of an integrated circuit (IC) is presented. In one embodiment, the pre-computed attribute is the intersect cost associated with a group of previously identified sub-region edges. In another embodiment, the pre-computed attribute is the usage cost associated with a group of previously identified sub-region paths. Such pre-computed numbers may be used by an electronic design automation application that identifies routes for nets in the IC region. --

MAILING ADDRESS OF SENDER: Stattler, Johansen & Adeli LLP
1875 Century Park East, Suite 1050
Los Angeles, CA 90067-2570**PATENT NO. 6,907,593 B1**

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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